



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Nestor A. BOJARCZUK, Jr., et al.

Serial No.: 10/059,422

Group Art Unit: 2814

Filing Date: January 31, 2002

Examiner: Doan, Theresa T.

For: METHOD OF FORMING LATTICE-MATCHED STRUCTURE ON SILICON
AND STRUCTURE FORMED THEREBY

Assistant Commissioner of Patents
Washington, D.C. 20231

**RESPONSE TO FIRST OFFICE ACTION AND
RESTRICTION REQUIREMENT**

Sir:

In response to the Office Action dated January 23, 2003, please consider the following:

REMARKS

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group II (e.g., drawn to a semiconductor structure as defined by claims 15-27 and 56-59), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

Early, favorable prosecution on the merits is respectfully requested.

Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.

#7/Election
Masha
2/26/03

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TECHNOLOGY CENTER 2800

Serial No.: 10/059,422
Docket No. YOR920010368US2
YOR.306CIP

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A conditional petition is made for any extension of time which may become necessary.
The Commissioner is authorized to charge any fees for such extension and to credit any
overpayment in fees to Assignee's Deposit Account No. 50-0510.

Respectfully Submitted,



Sean M. McGinn
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